

J. Matthew Zigmant, Reg. No. 44,005, (650) 326-2400
Titled: Method for Designing Mixed Signal Integrated Circuits
Configurable Synchronous Digital Noise Emulator Circuit
App. No. 09/618,971 Filed: July 19, 2000
Dckt. No.: 020408-000300US
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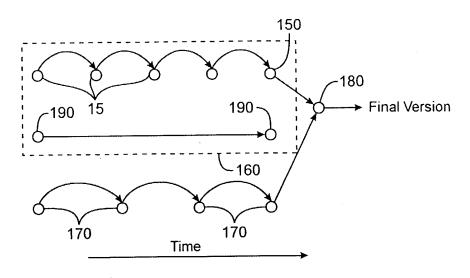


FIG. 1

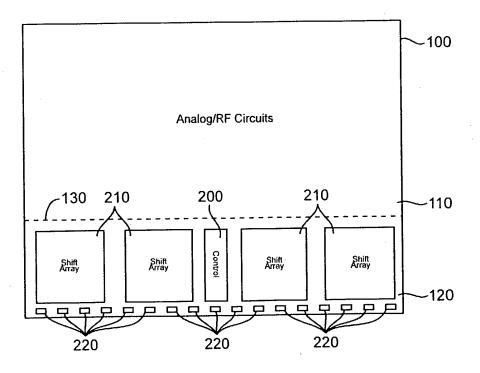
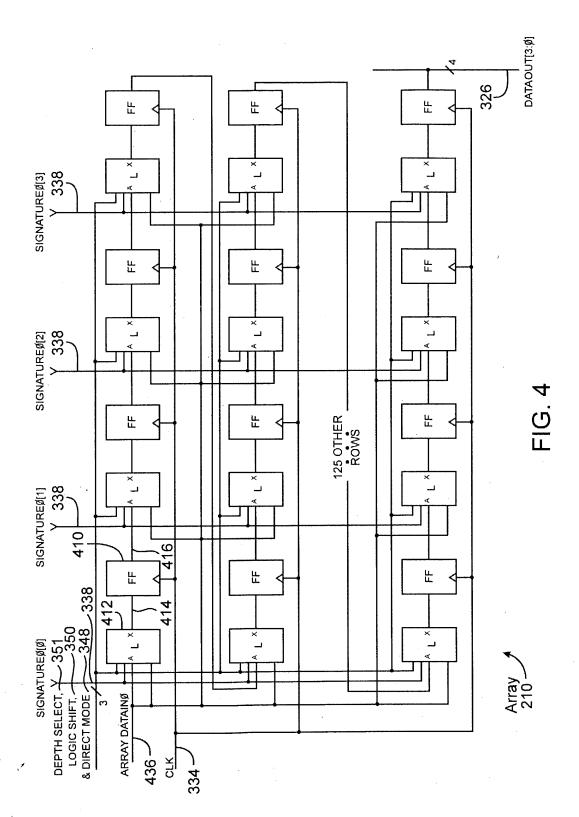


FIG. 2

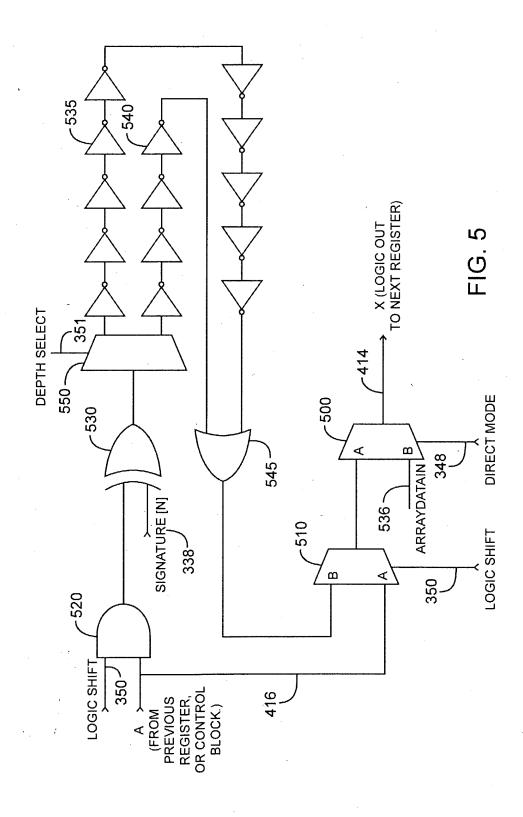
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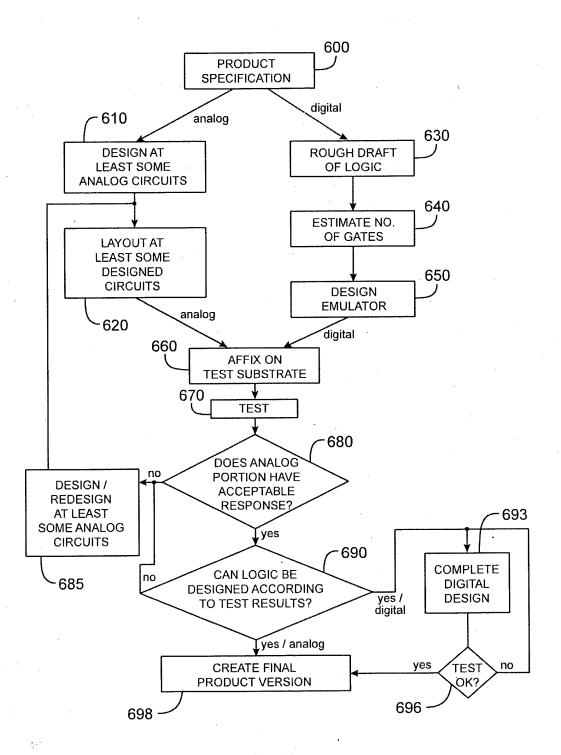


FIG. 6



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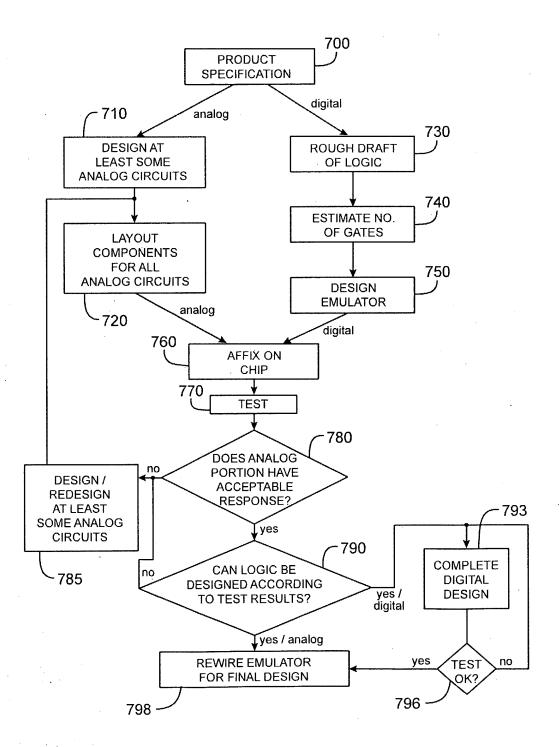
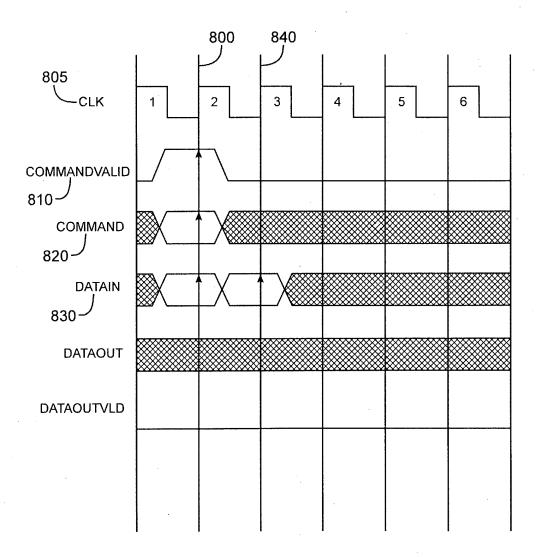


FIG. 7



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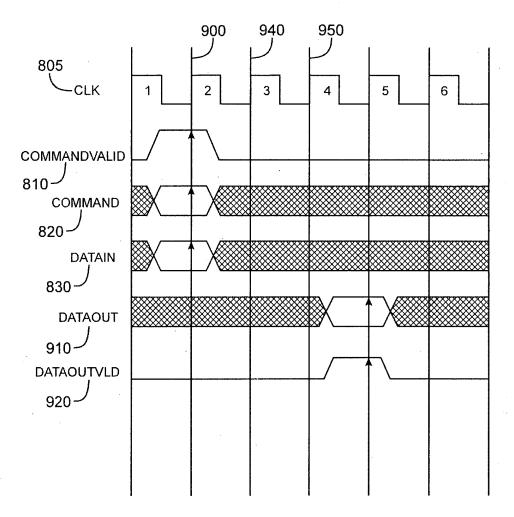


Configuration Register Write

FIG. 8



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Configuration Register Read

FIG. 9



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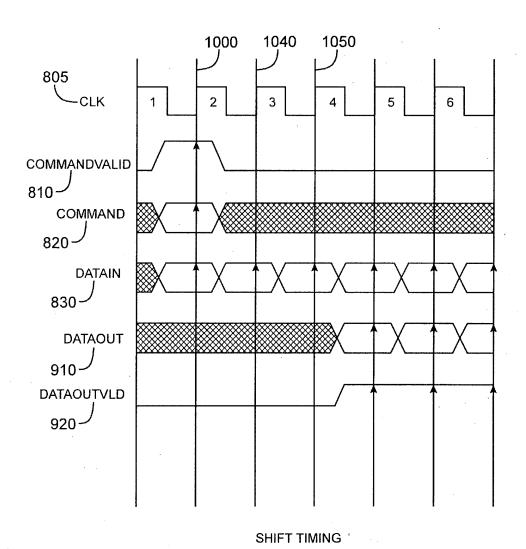


FIG. 10



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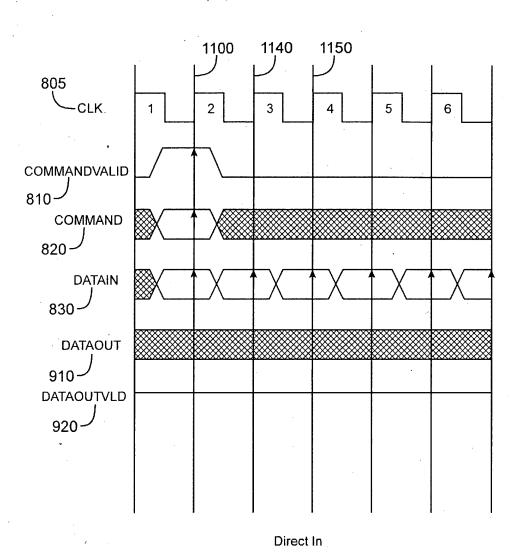


FIG. 11



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